

Form PTO-1449 (Rev. 2-32)		U.S. Department of Commerce Patent & Trademark Office		Atty. Docket No. Q65962	Serial No.: Confirmation No.: Unknown		
INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)		Applicant: Katsushi KIMURA					
		Filing Date: August 29, 2001		Group: Unknown			
U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Sub-Class	Filing Date (if appropriate)
<i>(Large grid area for listing U.S. Patent Documents)</i>							
FOREIGN PATENT DOCUMENTS							
		Document	Date	Country	Class	Sub-class	Translation Yes/No
M		2,556,293	09/05/96	Japan			Abstract
M		07-127887	05/16/95	Japan			Abstract
<i>(Large grid area for listing Foreign Patent Documents)</i>							
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
M		R. R. Torrance et al., "CMOS Voltage to Current Transducers", <i>IEEE Transactions on Circuits and Systems</i> , Vol. CAS-32, No. 11, November 1985, pp. 1097-1104					
M		K. Kimura, "MOS Linear and Square-Law Transconductance Amplifiers Consisting of a Source-Coupled Pair with Load Transistors and a Quadrant Cell Using Only N-Channel MOS Unit Transistors", <i>Technical Report of IEICE</i> , CAS98-41, NLP98-49, July 1998, pp. 17-24					
EXAMINER:		DATE CONSIDERED: 9/30/02					